



PATENT ABSTRACTS OF JAPAN

(11) Publication number: 2000349848 A

(43) Date of publication of application: 15.12.2000

(51) Int. Cl. H04L 27/227
H03J 7/18, H04N 5/455

(21) Application number: 11155215

(22) Date of filing: 02.06.1999

(71) Applicant: MATSUSHITA ELECTRIC IND CO LTD

(72) Inventor: AKAZAWA MIKIHIRO

(54) DIGITAL BROADCAST RECEIVER

(57) Abstract:

PROBLEM TO BE SOLVED: To provide a digital broadcast receiver that keeps a synchronization time within a prescribed time so as to attain high-speed channel selection and searching by controlling a carrier frequency to be demodulated in a way that a difference between a frequency of an intermediate frequency signal and the received carrier frequency to be demodulated by a demodulator is a prescribed value.

SOLUTION: Difference frequency detectors (9, 13) in the digital broadcast receiver (R) measure a difference destination (Δf_{1st}) between an intermediate frequency ($F(sif)$) and a carrier frequency ($F(Sca)$) to be demodulated. Reception control sections (13, 7) correct the carrier frequency ($F(Sca)$) so that the measured difference frequency (Δ) is within a 1st difference frequency region where a rate of change in a synchronization time with respect to the difference frequency between the intermediate frequency and the demodulation carrier frequency is small. Thus, the synchronization time (T_s) can be kept within a prescribed time by demodulating the intermediate fre-

quency ($F(sif)$) with the corrected demodulation carrier frequency ($F(Sca)$).

COPYRIGHT: (C)2000,JPO

